

2006 ASME/JSME Joint Conference on Micromechatronics for Information and Precision Equipment

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June 21-23, 2006, Santa Clara, California, USA

Participating Divisions: ASME Information Storage and Processing Systems (ISPS) Division
ASME Tribology Division, Symposium on Frontiers of Magnetic Hard Disk Drive Technology and Tribology
JSME Information, Intelligent and Precision (IIP) Equipment Division

Conference Chairs: I. Y. (Steve) Shen, University of Washington
Shozo Saegusa, Hiroshima University, Japan

Technical presentations are solicited for presentation at MIPE 2006. Relevant technologies include:

- Magnetic and optical devices in the form of rigid disk, flexible disk, or tape media
- Processing systems such as copiers, printers, digital cameras, scanners, and data servers
- Intelligent machines, biomedical equipment, and wearable systems

The following technical areas are indicative of the conference interdisciplinary scope: dynamics, vibrations, acoustics, tracking, friction, wear, lubrication, corrosion, contamination, thin films, contact mechanics, actuators, servo, motors and spindles, air flow, air-flow induced vibrations, heat transfer, image writing technologies (ink jet, thermal, laser printing, etc.), paper handling, roller mechanics, and image science.

Potential presenters should submit a one-page abstract for review. Please submit the one-page abstract to session organizers listed on the reverse side of the call for paper. Once the paper is accepted for presentation, the authors shall prepare a three-page extended abstract to be included in a conference abstract booklet. Presenters may also submit a full paper to be published in a Special Edition of *Microsystem Technologies* (impact factor: 0.732). Submitted papers will be peer-reviewed before publication.

The MIPE 2006 Conference will also host the following special features for conference attendees:

- a free copy of the Special Edition of *Microsystem Technologies* for each attendee with full and discount registration fees,
- a Best Paper Award,
- five Conference Fellowships to waive students' registration fees,
- reduced registration fees for students,
- discount registration fees for ASME and JSME members.

More detail information will be announced on the conference web site in due course.

Background of MIPE 2006:

In ASME (American Society of Mechanical Engineers), the ISPS (Information Storage and Processing Systems) Division is the major division working on disk drives, tape drives, optical drives, etc. ISPS has its annual conference held at San Jose in June every year. In JSME (Japanese Society of Mechanical Engineers), the IIP (Information, Intelligent, and Precision Machines) Division is devoted to information storage systems and other areas, such as precision machines and equipment. Similarly, IIP has its annual conference held in Japan. In year 2003, ISPS and IIP worked together to have a joint conference called MIPE (Micromechatronics for Information and Precision Equipment). The conference was held at Yokohama, Japan. Moreover, both ISPS and IIP merged their own annual conferences in 2003 into MIPE so that most people can attend. ISPS and



IIP agree to hold the joint MIPE conference every 3 years alternating between US and Japan. Moreover, the *Symposium on Frontiers of Magnetic Hard Disk Drive Technology and Tribology* from ASME Tribology Division will be held jointly in MIPE 2006.

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Executive Committee Chairs:

I. Y. (Steve) Shen, University of Washington
Shozo Saegusa, Hiroshima University, Japan

Executive Committee Members:

Neal Schirle	HGST
Sinan Muftu	Northeastern University
Jen-Yuan Chang	HGST
Jr-Yi Shen	HGST
Raymond A. de Callafon	University of California, San Diego
Masahiro Ohka	Nagoya University
Junguo Xu	Hitachi



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Advisory Committee Chairs:

US Co-Chair: Professor David B. Bogy, University of California, Berkeley.
Japan Co-Chair, Professor Yasunaga Mitsuya, Nagoya University.

Advisory Committee Members:

Bharat Bhushan	Ohio State University
David B. Bogy	University of California at Berkeley
Robert Evance	Hutchinson Technology
Hiroyuki Fujita	University of Tokyo
Guo GuoXiao	Data Storage Institute
Chong Tow Chong	Data Storage Institute
Hiromu Hashimoto	Tokai University
Mikio Horie	Tokyo Institute of Technology
Yiao-Tee Hsia	Seagate Technology
Koji Kato	Tohoku University
Hiroyuki Kawamoto	Waseda University
Dae-Eun Kim	Yonsei University
Mark Kryder	Seagate
Yuji Kubota	Toshiba Corp.
Ho Seong Lee	Samsung
Junichi Matsuno	Ricoh Printing Systems
Yoshifumi Mizoshita	Fujitsu
Yasunaga Mitsuya	Nagoya University
Yoshimasa Miura	Shishu University
Currie Munce	Hitachi Global Storage Technologies
Thao Nguyen	Maxtor Corporation
Kyosuke Ono	Hitachi
Kazuhiro OUCHI	Research Institute of Advanced Technology (AIT)
Young-Pil Park	Yonsei University
Frank Talke	University of California at San Diego
Koichi Tsuzuki	Hitachi R&D Group
David Whitaker	Western Digital
Jonathan Wickert	Carnegie-Mellon University
Ichiro Yamada	Tokyo University
Masahiro Yanagisawa	Waseda University
Timothy Hight	Santa Clara University

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The conference will be held at [Benson Memorial Center](#) of [Santa Clara University](#), Santa Clara, California, USA. The presentations will take place in the following three meeting rooms: [California Mission Room](#), [Williman Room](#), and [Parlors BCD](#).

Please click the following links to learn more about the conference venue.

- [Virtual Tour](#)
- [Directions](#)
- [Campus Map](#)
- [Parking](#)
- [Transportation](#)
- [Lodging](#)



2006 ASME/JSME Joint Conference on Micromechatronics for Information and Precision Equipment

Hotel Accommodations

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Hyatt San Jose (near San Jose Airport) is the contract hotel of MIPE 2006. As a courtesy, Hyatt San Jose has blocked a group of rooms for MIPE 2006 at the rate of \$115 for June 20-24, 2006. Availability of these blocked rooms is on a first-come-first-serve basis. The cutoff date is May 22, 2006. Please make reservation before the cutoff date to receive the conference room rate. The hotel address is:

Hyatt San Jose (Airport)
1740 N First Street
San Jose, CA 95112
[Hotel Web site](#)

For reservations:
Call: 800-233-1234, 888-975-1234, or
Fax: 408-453-0261
[Online reservations](#)

When you make your reservation, please indicate that you are attending the group "MIPE 2006" in order to get the conference rate.

For foreign attendees without a rental car, it is highly recommend that you stay in the contract hotel for several reasons. First, Hyatt San Jose Hotel provides shuttle bus service to San Jose International Airport. Second, MIPE 2006 organizing committee will arrange a shuttle bus running between Hyatt San Jose Hotel and the conference site at Santa Clara University. For additional information of the shuttle transportation to the conference site, please click [here](#).

Of course, it is not necessary to use the contract hotel. You can choose any hotel to stay during MIPE 2006. There are several hotels near the conference venue (Santa Clara University) within walking distance. To see a complete list of hotels near Santa Clara University, please visit the following web site: [Hotels near SCU](#).



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January 6, 2006:	1-page abstract due, submit 1-page abstract to session organizers.
February 10, 2006:	Notification of acceptance
March 21, 2006:	3-page extended abstract due for conference publication Email 3-page abstract to: jen-yuan.chang@hitachigst.com
May 1, 2006:	Nomination of ISPS Graduate Fellowships and MIPE Conference Fellowships (students only)
June 21-23, 2006:	MIPE 2006 Conference
June 23, 2006:	Full paper submitted to <i>Microsystems Technologies</i> , email the paper draft in PDF format to ishen@u.washington.edu .
Sep 30, 2006:	Notification of journal paper acceptance
Nov 25, 2006:	Author's response and revision due
June 30, 2007:	Publication of MIPE 2006 Special Issue



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Publication of research results at MIPE 2006 consists of a two-step process: conference abstracts and journal papers. They are explained in detail as follows.

Conference Abstract: Potential presenters should submit a one-page abstract for review. Please submit the one-page abstract to session organizers listed on MIPE 2006 conference web site. Once the paper is accepted for presentation, the authors shall prepare a three-page extended abstract to be included in a conference abstract booklet. Please download the abstract template. (pdf format, doc format)

The important deadlines are:

- | | |
|--------------------|--|
| January 6, 2006: | 1-page abstract due, submit 1-page abstract to session organizers; see reverse side. |
| February 10, 2006: | Notification of acceptance |
| March 21, 2006: | 3-page extended abstract due for conference publication |

Presenters could also submit a full paper to be published in a Special Edition of *Microsystem Technologies* (impact factor: 0.732). Submitted papers will be peer-reviewed before publication. To facilitate a prompt publication of the special issue, the following deadlines will be strictly imposed.

- | | |
|----------------|--|
| June 30, 2006: | Full paper submitted to <i>Microsystems Technologies</i> . Please submit your paper via the web site of Microsystem Technologies . |
| Sep 30, 2006: | Notification of journal paper acceptance |
| Nov 25, 2006: | Author's response and revision due |
| June 30, 2007: | Publication of MIPE 2006 Special Issue |

Journal Publication Submission: Please follow the procedure below to electronically submit your paper to MIPE 2006 Special Issue.

1. Go to the [Microsystem Technologies submission web site](#).
2. If you are a new user to Microsystem Technologies, please register first to create an account so that you can submit your paper. To register, please click the link "REGISTER" under the Microsystem Technologies logo.
3. To submit a paper, please click the link "SUBMIT A MANUSCRIPT" under the Microsystem Technologies logo. Login the system (called Editorial Manager) using your username and password.
4. Enter the title of your paper.
5. Choose the article type. There is a pull-down menu. Please select "Special Issue MIPE 2006." This is a critical step. It ensures that your paper will appear in the Special Issue.
6. The remaining submission is very straightforward by entering the authors, abstracts, keywords, text and graphic files.
7. When attaching your files, please note that the system does NOT accept PDF files. Instead, the system will build a PDF file for you.
8. Please click the link "INFORMATION FOR AUTHORS" under the Microsystem Technologies logo for additional information.
9. After you successfully upload your files and finish your submission, please fill out the Copyright Transfer Statement and mail or fax it back to Microsystem Technologies. Please see detailed instructions at the [Microsystem Technologies submission site](#).



2006 ASME/JSME Joint Conference on Micromechatronics for Information and Precision Equipment

Session Organizers

Links	Session Name	Organizer	Email
Home	Tribology Symposium (Tribology Division)	Yiao-Tee Hsia (Seagate)	yiao-tee_hsia@seagate.com
		Mike Suk (HGST)	Mike.Suk@hgst.com
Executive Committee	Tribology, Head/Disk Interface	Frank Talke (UC San Diego)	ftalke@ucsd.edu
		Bo Liu (DSI)	Liu_Bo@dsi.a-star.edu.sg
		T. Yamamoto (Toshiba)	takayuki.yamamoto@toshiba.co.jp
Advisory Committee		Junguo Xu (Hitachi)	Junguo.Xu@hitachigst.com
Call for Papers	Spindle Motor & Acoustics	Steve Shen (U Washington)	ishen@u.washington.edu
		Gunhee Jang (Hanyang U)	ghjang@hanyang.ac.kr
		Junichi Ichihara (Fujitsu Lab)	ichij@flab.fujitsu.co.jp
Conference Venue	Actuators & Suspensions	Rob McNab (Western Digital)	robert.mcnab@wdc.com
		Kenji Aruga (Fujitsu)	aruga@jp.fujitsu.com
		Sean Kang (Western Digital)	seong-woo.kang@wdc.com
Hotel Accommodations	HDD Servo Control	Raymond de Callafon (UCSD)	callafon@ucsd.edu
		Lin Guo (Maxtor)	lin_guo@maxtor.com
		Guoxiao Guo (DSI)	GUO_Guoxiao@dsi.a-star.edu.sg
		William Messner (CMU)	bmessner@andrew.cmu.edu
Important Deadlines	Flow-Induced Vibration	Srinivas Tadepalli(Seagate)	srinivas.tadepalli@seagate.com
		Norio Tagawa (Kansai U)	tagawa@ipcku.kansai-u.ac.jp
Publication	Flexible Media Mechanics and Tape Storage	Richard Jewett (Imation)	rejewett@imation.com
		Hiromu Hashimoto (Tokai U)	hiromu@keyaki.cc.u-tokai.ac.jp
Registration	Optical Storage	No-Cheol Park (Yonsei U)	pnch@yonsei.ac.kr
		Takeshi Shimano (Hitachi)	shimano@rd.hitachi.co.jp
ISPS Scholarships	Image Construction Technology	George Chiu (Purdue U)	gchiu@purdue.edu
		Hiroyuki Kawamoto (Waseda U)	kawa@waseda.jp
Session Organizers	Micro/Nano-technologies	Toshiki Hirano (Hitachi GST)	Toshiki.Hirano@hitachigst.com
		Lin Wu (U Nebraska)	linwu@unlserve.unl.edu
		Kenji Fukuzawa (Nagoya U)	fukuzawa@nuem.nagoya-u.ac.jp
Technical Programs	Bio-medical Equipment and Intelligent Machines	Behrad Khamesee (U. Waterloo)	khamesee@mecheng1.uwaterloo.ca
		Masahiro Ohka (Nagoya U)	ohka@is.nagoya-u.ac.jp
		Mami Tanaka (Tohoku U)	mami@rose.mech.tohoku.ac.jp
Workshops	Future Technology	ONG Eng Hong (DSI)	ONG_Eng_Hong@dsi.a-star.edu.sg
Tourist Information	Operational and Non-Operational Shock Analysis	Srinivas Tadepalli(Seagate)	srinivas.tadepalli@seagate.com
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Technical Programs

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Program Schedule

Day 1	
8:00 am -- 5:00 pm	Registration
8:15 am -- 9:00 am	Continental Breakfast (Williman Patio)
8:45 am -- 9:00 am	Conference Welcome (California Mission Room)
9:00 am -- 10:40 am	Technical sessions (5 papers, 20 min each)
10:40 am -- 11:00 am	Coffee break
11:00 am -- 12:40 am	Technical sessions (5 papers, 20 min each)
12:40 pm -- 1:40 pm	Lunch
1:40 pm -- 3:20 pm	Technical sessions (5 papers, 20 min each)
3:20 pm -- 3:40 pm	Coffee break
3:40 pm -- 5:20 pm	Technical sessions (5 papers, 20 min each)
5:30 pm -- 6:30 pm	ISPS Executive Committee Meeting (Parlor A)
Day 2	
8:00 am -- 5:00 pm	Registration
8:15 am -- 9:00 am	Continental Breakfast (Williman Patio)
9:00 am -- 10:40 am	Technical sessions (5 papers, 20 min each)
10:40 am -- 11:00 am	Coffee break
11:00 am -- 12:40 am	Technical sessions (5 papers, 20 min each)
12:40 pm -- 1:40 pm	Lunch
1:40 pm -- 3:20 pm	Technical sessions (5 papers, 20 min each)
3:20 pm -- 3:40 pm	Coffee break
3:40 pm -- 5:20 pm	Technical sessions (5 papers, 20 min each)
5:25 pm -- 6:30 pm	Hors d'oeuvres, no host bar (Williman Patio)
6:30 pm -- 7:30 pm	Dinner (California Mission Room)
7:30 pm -- 8:30 pm	Guest Speaker (Michael Russak, EVP, CTO, Komag)
Day 3	
8:00 am -- 5:00 pm	Registration
8:15 am -- 9:00 am	Continental Breakfast (Williman Patio)
9:00 am -- 10:40 am	Technical sessions (5 papers, 20 min each)
10:40 am -- 11:00 am	Coffee break
11:00 am -- 12:40 am	Technical sessions (5 papers, 20 min each)
12:40 pm -- 1:40 pm	Lunch
1:40 pm -- 3:20 pm	Technical sessions (5 papers, 20 min each)
3:20 pm -- 3:40 pm	Coffee break
3:40 pm -- 5:20 pm	Technical sessions (5 papers, 20 min each)



5:45 pm -- 8:00 pm	ISPS/IIP/MIPE Executive Committee Dinner (Santa Clara University Faculty Club)
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If there are too many papers, we can shorten the presentation time or to use poster sessions.

Sessions

For more detailed information about the sessions, see the schedule in [Excel format](#)

	California Mission	Parlors B, C, D	Williman
Estimated Attendees	70-80	40-50	20-30
Day 1			
AM Session 1 (5 papers)	HDI (ISPS Division)	Actuator	Bio & Intelligent Equipment
AM Session 2 (5 papers)	HDI (ISPS Division)	Actuator	Bio & Intelligent Equipment
PM Session 1 (5 papers)	MEMS/Nano	Actuators+Operating Shock	Flexible Media & Tapes
PM Session 2 (5 papers)	MEMS/Nano	Operating Shock	Flexible Media & Tapes
Day 2			
AM Session 1 (5 papers)	HDI (ISPS Division)	Flow Induced Vibration	Bio & Intelligent Equipment
AM Session 2 (5 papers)	MEMS/Nano	Flow Induced Vibration	Flexible Media & Tapes
PM Session 1 (5 papers)	MEMS/Nano	Spindle Motors	Optical Drives
PM Session 2 (5 papers)	MEMS/Nano	Spindle Motors	Optical Drives
Day 3			
AM Session 1 (5 papers)	HDI (Tribology Symposium)	Image Construction	Spindle Motors
AM Session 2 (5 papers)	HDI (Tribology Symposium)	Image Construction + Future Technology	Optical Drives
PM Session 1 (5 papers)	HDI (Tribology Symposium)	HDD Servo	Optical Drives
PM Session 2 (5 papers)	HDI (Tribology Symposium)	HDD Servo	Optical Drives

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Visitor Information

For local attractions, dining, shopping, and recreation information in the San Jose area, please visit the following web sites.

- [San Jose Convention & Visitors Bureau](#)
- [Santa Clara Convention & Visitors Bureau](#)
- [San Francisco Convention & Visitors Bureau](#)

On-Line Tour Reservation

We have collected several online tour reservation links listed below. These companies offer popular San Francisco Bay Area tours at your choice of attractions. Please note that none of these companies has a contract with MIPE 2006 on guided tours. We are providing the links as a service to MIPE participants.

- [San Francisco Charters and Tours Systems](#) (pick-up and drop-off at Hyatt San Jose Hotel)
- [San Francisco Sightseeing with Gray Line](#) (Visitors are responsible to go to Pier 43, San Francisco by departure time)
- [San Francisco Tours](#) (pick-up and drop-off at San Francisco/SFO Airport hotels)



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SAN FRANCISCO AIRPORT TO HYATT SAN JOSE

There are several ways to travel from [San Francisco Airport](#) to [Hyatt San Jose](#), such as shuttle buses, taxi, and public transportation. They are described as follows.

1. Shuttle Buses (Door-to-Door Vans): Van service is available on a walk-up basis. Door-to-door vans pick up on the Departures/Ticketing Level from the roadway Center Island at all terminals. The fare is approximately \$65-75. For those who like to plan ahead, you can make shuttle reservation before your arrival through the following companies.

- [San Francisco Charters and Tours](#)
- [Shuttle Pros](#)
- [SOS Shuttle](#)
- [Super Shuttle](#)

2. Taxi: Taxis depart from the designated taxi zones located at the roadway Center Islands, on the Arrivals/Baggage Claim Level of all terminals. The approximate fare is \$135 to San Jose.

3. Public Transportation: It is very inconvenient to use public transportation to go from San Francisco Airport to Hyatt San Jose. This option is not recommended especially for foreign travelers.

SAN JOSE AIRPORT TO HYATT SAN JOSE HOTEL

Hyatt Hotel provides a courtesy service between San Jose International Airport and the hotel. When you arrive at the San Jose International Airport, please call from the courtesy phone near the baggage claim area. You can also find more [detail at this link](#).

HYATT SAN JOSE HOTEL TO/FROM SANTA CLARA UNIVERSITY

The conference has arranged a complimentary shuttle bus service between Hyatt San Jose and the conference venue Santa Clara University. The shuttle bus service is operated by [San Francisco Charters and Tours](#). The tentative schedule is:

	June 21	June 22	June 23
Hyatt to Santa Clara University	8:15am	8:15am	8:15am
Santa Clara University to Hyatt	6:00pm	9:00pm	6:00pm

Please note that this is a tentative schedule. We will finalize the schedule when the final program is available.



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Contact MIPE 2006 Chairs

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